

Form PTO 1449 (Modified)		U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE		ATTY DOCKET NO. 250848US2S		SERIAL NO. New Application		
LIST OF REFERENCES CITED BY APPLICANT		APPLICANT						
		Osamu HIRABAYASHI						
		FILING DATE Herewith		GROUP				
U.S. PATENT DOCUMENTS								
EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUB CLASS	FILING DATE IF APPROPRIATE	
	AA							
	AB							
	AC							
	AD							
	AE							
	AF							
	AG							
	AH							
	AI							
	AJ							
	AK							
	AL							
	AM							
	AN							
FOREIGN PATENT DOCUMENTS								
		DOCUMENT NUMBER	DATE	COUNTRY	TRANSLATION			
					YES	NO		
	AO							
	AP							
	AQ							
	AR							
	AS							
	AT							
	AU							
	AV							
OTHER REFERENCES (Including Author, Title, Date, Pertinent Pages, etc.)								
<i>SDR</i>	AW	C. L. CHEN, et al., "Error-Correcting Codes for Semiconductor Memory Applications: A State-of-the-Art Review", IBM J. RES. DEVELOP, Vol. 28, No. 2, March 1984, pgs. 124-134						
	AX							
	AY							
	AZ					<input type="checkbox"/> Additional References sheet(s) attached		
Examiner	<i>Osamu Hirabayashi</i>				Date Considered <i>3/3/06</i>			

*Examiner: Initial if reference is considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.